

Abstract of the Disclosure

A system and method allows input/output to and from a computer system via the memory bus of a computer system.

Input is accepted directly into shared memory or other

5 memory and assigned to a processor or other entity. A

processor or other entity may retrieve the input assigned

to it and process the input. Output is written by the

system processors to private memory, then retrieved from

private memory by the system and method and output, for

10 example to a network. Inputs and outputs are written to

and from memory without using the I/O buses of the computer

system and without generating interrupts to the system

processors.